

Atty. Docket No.: NLMLP109

PATENT

IN THE UNITED STATES PATENT OFFICE

In Re Patent Application of:

First Named Inventor: Nataraj, Bindiganavale S.

Application No.: 10/000,122

Filed: 10/31/2001

Title: CONTENT ADDRESSABLE MEMORY WITH
PROGRAMMABLE WORD WIDTH AND
PROGRAMMABLE PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Examiner: Elmore, Reba I.

Art Unit: 2187

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450 on January 10, 2005

Charles E. Shemwell

(Name of Person Mailing Correspondence)

Charles E. Shemwell 1/10/2005
Signature Date

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

Sir:

Enclosed is an Information Disclosure Citation Form 1449/PTO together with a copy of each reference cited therein, excluding U.S. Patents and Published U.S. Patent Applications. It is respectfully requested that the cited references be considered and that the enclosed copy of the Form 1449/PTO be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant.

This Information Disclosure Statement is being submitted pursuant to 37 CFR 1.97(c). A check to cover the \$180.00 fee under 1.17(p) is enclosed.

Pursuant to 37 CFR 1.97(h), the submission of this Information Disclosure Statement is not to be construed as an admission that the information cited in this statement is material to patentability.

The Commissioner is hereby authorized to charge any fees which may be required in connection with this submission to Deposit Account No. 501914.

Respectfully submitted,

SHEMWELL GREGORY & COURTNEY LLP

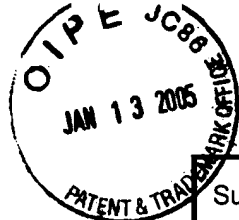
Charles E. Shemwell
Charles E. Shemwell, Reg. No. 40,171
Tel. 408-236-6645

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Date January 10, 2005



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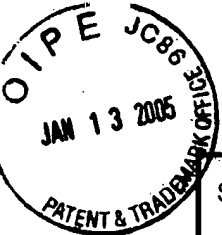
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Application No.	10/000,122
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First Inventor	Nataraj, B.
Art Unit	2187
Examiner	Elmore, Reba I.
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Examiner Initials*	US Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication	Relevant Pages, Columns, Lines
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	3602899		Lindquist et al.	Aug., 1971	
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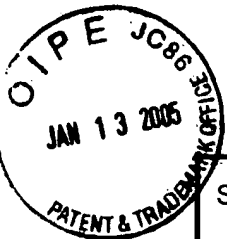
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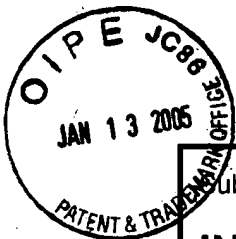
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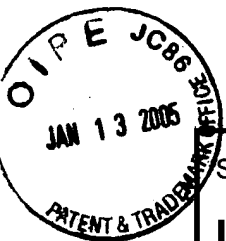


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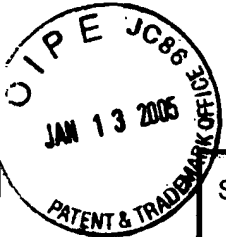
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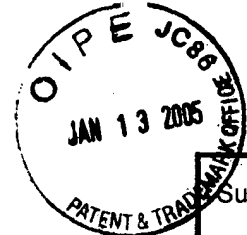


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	6374326		Kansal et al.	Apr., 2002	
	6381673		Srinivasan et al.	Apr., 2002	
	6389506		Ross et al.	Aug, 1998	
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	6502163		Ramankutty	Dec., 2002	
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U.S. Patent Documents

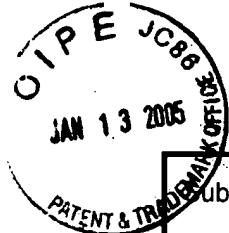
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	Number	Kind			
	6542391		Pereira et al.	Apr., 2003	
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	6574702		Khanna et al.	Jun., 2003	
	6591331		Khanna	Jul., 2003	
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	6678786		Srinivasan et al.	Jan., 2004	
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Foreign Patent Documents

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	Number	Kind				
	EP 0267612	A2		May, 1988		
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	EP 0858077			Aug., 1998		
	EP 0918335			May., 1999		
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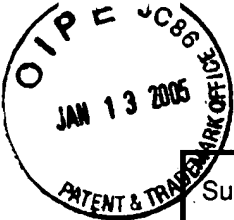
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	WO 99/31581			Jun., 1999		

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Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Trans- lation
	Gupta et al., "Packet Classifications on Multiple Fields," SIGCOMM (September 2, 1999) 14 pages.	
	McAuley et al., "A Self-Testing Reconfiguration CAM", IEEE Journal of Solid-State Circuits, Vol.26, No. 3, March 1991, pp.257-261	
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	KSLI KE5B064H Series Address Processor Ver. 1.0.1 (believed to be published late 1995 or early 1996).	
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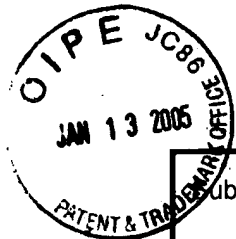
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	Music Semiconductors, "MU9C1480 LANCAM Handbook" November 1994.	
	Ghose et al., "Response Pipelined CAM Chips: The First Generation and Beyond," 7th International Conference on VLSI Design, January 1994, pp. 365-368.	
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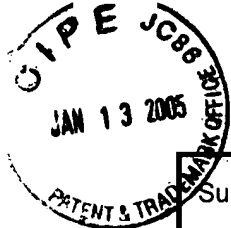
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	Ramirez-Chavez, "Encoding Don't Cares in Static and Dynamic Content-Addressable Memories", Transaction Briefs, IEEE transactions on Circuits and Systems-II: Analog and Digital Processing, Vol. 39, No 8, August 1992, pp. 575-578.	
	Wang et al., "Realization of Bidirection Association Memory Using a Pseudo-Parallel Searching Approach," 1995 IEEE International Conference on Neural Networks, Vol. 3, pp. 1502-1507, December 1995.	
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	Mosaid Technologies Inc., "The Next Generation of Content Addressable Memories," September 1999, 8 pages.	

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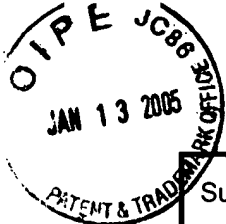
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Examiner Signature		Date Considered	
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First Inventor	Nataraj, B.
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Non Patent Literature Documents

Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Trans- lation
	Shin et al., "A Special-Purpose Content Addressable Memory Chip for Real-Time Image Processing," IEEE Journal of Solid-State Circuits, Vol. 27, No. 5, May 1992, pp. 737-744	

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